

REMARKS

Claims 1-16 and 18-19 were pending in the present application. Claims 1-16 and 18-19 have been cancelled. Claims 20-40 have been added. Accordingly, claims 20-40 are now pending in the application.

Claims 16 and 18 stand rejected under 35 U.S.C. §102(b) as being anticipated by Schmidt (U.S. Patent Publication No. 2002/0128037). Although Applicant respectfully traverses portions of this rejection, Applicant has cancelled the claims and added new claims. Accordingly, Applicant submits the new claims distinguish over Schmidt for the reasons given below. Support for the new claims may be found in the specification at least at page 7, lines 13-18 and in the cancelled claims.

Claims 1-15, and 19 stand rejected under 35 U.S.C. §103(a) as being unpatentable over Schmidt in view of Lee (U.S. Patent No. 5,490,101). Although Applicant respectfully traverses portions of this rejection, Applicant has cancelled the claims and added new claims. Accordingly, Applicant submits the new claims distinguish over Schmidt for the reasons given below. Support for the new claims may be found in the specification at least at page 7, lines 13-18 and in the previous cancelled claims.

Applicant's claim 20 recites a wireless communication device comprising:

- a host processor configured to execute instructions belonging to an instruction set of a first processor family; and
- a reconfigurable processor core coupled to the host processor, wherein the reconfigurable processor core includes a plurality of processors, wherein portions of the plurality of processors are selectably configurable to execute instructions belonging to a plurality of instruction sets;
- wherein a first portion of the plurality of processors is configured to execute instructions belonging to an instruction set of the first processor family;
- wherein a second portion of the plurality of processors is configured to execute instructions belonging to an instruction set of a second processor family; and
- wherein the host processor and the reconfigurable processor core are both located on a single integrated circuit die. (Emphasis added)

Applicant notes support for the present claims includes the passage at page 7, lines 13-18 of the specification:

Moreover, a portion of the reconfigurable processor core **150** can be organized as a second host processor processing instructions belonging to a second host processor family, while the host processor **148** belongs to a first processor family that processes instructions belonging to the first host processor family instruction set. In one embodiment, the host processor **148** is MIPS compatible, while the second host processor portion of the processor core **150** can be ARM compatible.

Applicant notes that the above-quoted passage does not appear in Schmidt. Applicant respectfully submits that paragraphs 16 and 17 cited by the Examiner at page 4 of the present Office Action do not teach, among other things, “wherein portions of the plurality of processors are *selectably configurable* to execute instructions belonging to a plurality of instruction sets.”

For at least the reasons given above, Applicant submits that the cited art does not teach the above-noted features of claim 20. (Applicant notes that Lee is relied upon only as teaching integrating more than one function on a single chip, and is not relied upon to teach the above-noted claim limitations). Thus, Applicant’s claim 20, along with its dependent claims patentably distinguishes over Schmidt in view of Lee for at least the reasons given above.

Applicant’s claims 33 and 40 include features that are similar to the features recited in claim 20. Accordingly, for at least the reasons given above claims 33 and 40, along with their respective dependent claims patentably distinguish over Schmidt in view of Lee.

CONCLUSION

Applicant submits the application is in condition for allowance, and an early notice to that effect is requested.

If any fees are due, the Commissioner is authorized to charge said fees to Meyertons, Hood, Kivlin, Kowert, & Goetzel, P.C. Deposit Account No. 501505/6057-61200/SJC.

Respectfully submitted,

/ Stephen J. Curran /

Stephen J. Curran
Reg. No. 50,664
AGENT FOR APPLICANT(S)

Meyertons, Hood, Kivlin, Kowert, & Goetzel, P.C.
P.O. Box 398
Austin, TX 78767-0398
Phone: (512) 853-8800

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